



AF/ #2003
Appeal
Received
9/12/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Douglas T. Grider Docket No: TI-31118
Serial No: 09/967,044 Conf. No: 4815
Examiner: Julio J. Maldonado Art Unit: 2823
Filed: 09/28/2001
For: METHOD FOR TRANSISTOR GATE DIELECTRIC LAYER WITH UNIFORM NITROGEN CONCENTRATION

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF APPEALS

Mail Stop AF
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(a)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 8-19-03.


Ann Trent

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner mailed May 20, 2003, finally rejecting Claims 1-10.

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AUG 29 2003

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The item(s) checked below are appropriate:

1. An extension of time to respond to the final rejection

was granted on _____.
 is requested for ____ month.

2. A timely response to the final rejection has been filed.

3. Fee \$320.00:

Not required (Fee paid in prior appeal)
 The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.
This form is submitted in triplicate.


Peter K. McLarty
Attorney for Appellant
Reg. No. 44,923

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